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#### Panel: HARDWARE/SOFTWARE CO-VERIFICATION

... between hardware and software so tools can work across **multiple** applications:

... There is no single hardware-software co-**simulation** problem and as a ...

[www.sigda.org/Archives/ProceedingArchives/Dac/Dac97/papers/1997/dac97/htmfiles/sun\\_sgi/panel\\_29.htm](http://www.sigda.org/Archives/ProceedingArchives/Dac/Dac97/papers/1997/dac97/htmfiles/sun_sgi/panel_29.htm) -

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#### SIGDA Super Compendium, DAC 1997, Table of Contents

... Panelists: **Brian Bailey**, Kurt Keutzer, Amr Mohsen, Richard Moseley, ...

38.1 Fault **Simulation** under the **Multiple** Observation Time Approach using ...

[www.sigda.org/Archives/ProceedingArchives/Compendiums/papers/1997/dac97/htmfiles/sun\\_sgi/dactoc.htm](http://www.sigda.org/Archives/ProceedingArchives/Compendiums/papers/1997/dac97/htmfiles/sun_sgi/dactoc.htm) -

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#### [PDF] Accellera C Standard Group Meeting

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... In RTL is a clock a single event or does it have **multiple** phases that can be

used. ... A-12 Formal invite to Chris and Joe to regisiter. **Brian Bailey** ...

[www.eda.org/alc-cwg/open/Minutes0803.PDF](http://www.eda.org/alc-cwg/open/Minutes0803.PDF) - [Similar pages](#)

#### [PDF] Accellera ALC – C++ Initiative Working Group meeting 09/072000

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... Undefined is for **simulation**. Don't care is for synthesis – a directive. ...

Contact Steve Shultz about contributing to this group (**Brian Bailey**) ...

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... Modeling and **Simulation** of Mixed-Signal Systems with SystemC ... prototype but with intermittent and discontinuous **EDA** support. ...

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#### DATE Technical Programme Committees

... component-based modeling, **simulation** and design of multi-core SoC ... Chair:

**Brian Bailey**, Mentor, UK, Co Chair: Wolfgang Ecker, Infineon, Germany ...

[www.date-conference.com/cgi-bin/show\\_committees\\_tpc\\_list.cgi](http://www.date-conference.com/cgi-bin/show_committees_tpc_list.cgi) - 75k - [Cached](#) - [Similar pages](#)

#### [PDF] Tuesday, June 10 Wednesday, June 11

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... papers cover improved fault **simulation**. procedures based on **multiple** ...

**EDA** tools will move towards a multi-tiered and client-server paradigm in order ...

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... largely ignored by the commercial **EDA** tools ... Switching Module **Multiple** Pentiums 2MB+

**Multiple** ASICs from ... present a hardware/software co-**simulation** challenge: the ...

[newit.gsu.unibel.by/resources/Articles%5CMentor%5Ccosim\\_strategies.pdf](http://newit.gsu.unibel.by/resources/Articles%5CMentor%5Ccosim_strategies.pdf) - Supplemental Result - [Similar pages](#)

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... A language for hybridsystem engineering modeling and **simulation**. ... **Brian Bailey**.

Extending traceswith OAT: An object attri bute trace package for ...

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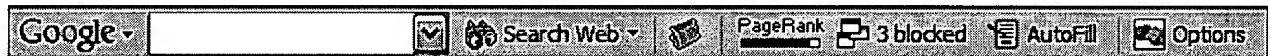
... aus den Bereichen Halbleiter, IP, **EDA** und Em ... Verification and Management of a multi-million gate ... Serge Leef: Hardware/Software Co-**Simulation** Strategies for the ...

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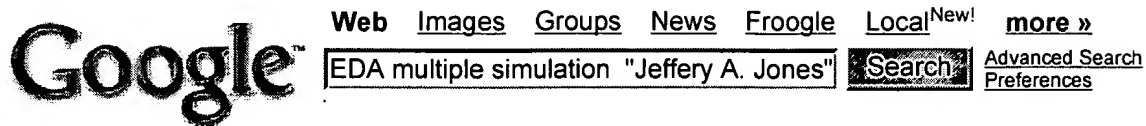
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